

Jessica Heider

(817) 917-6074 • jessieheider@gmail.com

Skills

Programming Languages C/C++, Python, some Java, MATLAB, Assembly, Verilog/VHDL

Development Tools Git, Atlassian tools, lab bench equipment, Jenkins, Altium

Work Experience

Cirrus Logic, Austin, TX

May 2022 - July 2023

Applications Engineer - Power Products

- Supported customers through email, calls, and onsite visits
- Performed validation on customer returns
- Developed automated python scripts for customer demos and hardware tests
- Tested and debugged circuits
- Designed schematics
- Wrote Verilog for FPGA development

Texas Instruments, Dallas, TX

Applications Engineer - Battery Gauges

OCTOBER 2020- April 2022

- Supported customers through Jira, email, and TI's online forum
- Developed software for I2C/ SMBus to USB battery gauge communication tool
- Designed EVM schematics for next generation battery gauge and communication tool
- Performed validation on customer returns and custom firmware

Embedded Software Engineer

June 2017 - October 2020

- Worked on a team to develop TI SDKs for embedded processors
- Designed APIs and software architecture for hardware peripherals for next generation microcontrollers
- Wrote RTOS based drivers, tests, and examples in C for various hardware modules including ADC, DMA, UART, and SPI
- Created hardware layer code for peripherals
- Tested and released SDK packages to the public
- Gave customer support for BLE devices through TI's online support forum

Leadership at TI

- Intern mentor
- University Recruiter at the University of Texas in Austin
- Gave tech talks at the University of Texas in Austin and the University of Texas in Dallas

Education

University of Texas, Austin, TX

B.S. Mathematics, B.S. Electrical & Computer Engineering

AUGUST 2012 - MAY 2017

Overall GPA: 3.72/4.0